

ABSTRACT OF DISCLOSURE

1 A low weight encoding circuit of a power delivery system for encoding data sent out on
2 an I/O bus with minimal current drawn so as to minimize signal and timing distortions. Such a
3 low weight encoding circuit comprises a current balance tester arranged to test whether a
4 predetermined number of data bits is current balanced; a current balance encoder and decode bit
5 generator arranged to encode data bits and generate encoded data and corresponding decode bits
6 if the predetermined number of data bits is not current balanced; and a latch arranged to latch
7 either the data bits, via an I/O bus, if said predetermined number of data bits is current balanced
8 or the encoded data and corresponding decode bits, via the I/O bus, if the predetermined number
9 of data bits is not current balanced.
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